## CLAIMS

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 $m{\lambda}$ . A universal graph compilation system comprising microcomputer (23) connected to at component (35) on which must be implemented the command corresponding to a graph, characterized in that order to simplify the entry of the graphs, this system comprises a man-machine interface (24) implemented on the microcomputer (23) where it is connected to a which is compiler (29) itself connected via operating system  $\Delta f$  the microcomputer (32) to means of writing in at least one memory -(34) of the component the man-machine interface comprising spreadsheet (24) associated with a library (25) of two types of graphical symbols each one corresponding, with regard to the first elementary type, to an component function and, with regard to the second type, to a link relating to the symbols of the first type, the symbols selected in the library being placed in the spreadsheet at a rate of one symbol per cell or per group of cells and assembled in such a way as to constitute a graph, each of the graphical symbols being represented in a group of adjacent elementary squares, and their connections ending at the centers of corresponding sides of each elementary square.

The system as claimed in claim 1, characterized in that the memories in which the components are written are connected directly to the microcomputer (33-34).

- 3. The system as claimed in one of the preceding claims, characterized in that the memories (34+) in which the commands must be written are fixed on the corresponding components (35+) and in that these commands are remote loaded (37).
- 4. The system as claimed in one of the preceding claims, characterized in that the man-machine interface comprises a topological checker (26), and a syntactic and semantic checker (27).

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